



Imaj ka reprezantasyon.
Gade espesifikasyon pou detay pwodwi.

XCV50-6TQ144C

Nimewo Pati: **XCV50-6TQ144C**
 Deskripsyon pwodwi: IC FPGA 98 I/O 144TQFP
 RoHs Status: Gen plon / RoHS ki pa konfòme
 Datasheets: [XCV50-6TQ144C.pdf](#)

Manifakti / Brand: Xilinx
 Bato de: Hong Kong
 Shipment Way: DHL/Fedex/TNT/UPS/EMS

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Pwodwi detay

Nimewo Pati	XCV50-6TQ144C	Manifakti	Xilinx
Deskripsyon	IC FPGA 98 I/O 144TQFP	Plon gratis Status / RoHS Status	Gen plon / RoHS ki pa konfòme
Done fèy	XCV50-6TQ144C.pdf		
Voltage - Pwovizyon pou	2.375 V ~ 2.625 V	Total Bits RAM	32768
Founisè Aparèy pake	144-TQFP (20x20)	Seri	Virtex®
Pake / Ka	144-LQFP	Operating Tanperati	0°C ~ 85°C (TJ)
Kantite Eleman Lojik / Selil	1728	Kantite LAB / CLBs	384
Kantite I / O	98	Nimewo nan Gates	57906
Mounting Kalite	Surface Mount	Imidite sansiblite Nivo (MSL)	3 (168 Hours)
Plon gratis Status / RoHS Status	Contains lead / RoHS non-compliant	Nimewo Pati Base	XCV50

Pwodwi ki gen rapò

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